

#3-6-02 B

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

J1017 U.S. PTO
10/043468
01/10/02

In re Application of:

Warren M. Farnworth

Serial No.: Not Yet Assigned

Filed: January 10, 2002

For: METHOD OF FORMING
OVERMOLDED CHIP SCALE PACKAGE
AND RESULTING PRODUCT

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 3085.4US (96-1033.4)

NOTICE OF EXPRESS MAILING

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

The present application is a continuation of application Serial No. 09/478,386, filed January 6, 2000, pending, which is a divisional of application Serial No. 09/304,368, filed May 4, 1999, now U.S. Patent 6,204,095 B1, issued March 20, 2001, which is a continuation of application Serial No. 09/056,124, filed April 6, 1998, now U.S. Patent 5,933,713, issued August 3, 1999.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior applications, and to confirm in the first Office Action on the

merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior applications is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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ERC/ps:jb

Enclosure: Form PTO-1449

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